

10/ 764 243

107 ✓



PATENT
143461

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 6,963,203 B2

Issued: November 8, 2005

Inventor(s): Stanasic et al.

Assignee: General Electric Company

For: METHODS AND APPARATUS FOR
ANALYZING HIGH VOLTAGE CIRCUIT
BREAKERS

Certificate
SEP 06 2007
of Correction

CERTIFICATE OF MAILING

I certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 31, 2007.

Robert B. Reesor, III
Reg. No. 45658

Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF
PATENT UNDER 37 C.F.R. 1.322(a)

Sir:

Attached is Form PTO/SB/44 suitable for printing. Applicants respectfully request a Certificate of Correction for the following:

In Claim 15, column 12, line 24, delete "programed" and insert therefor --
programmed --.

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PATENT
143461

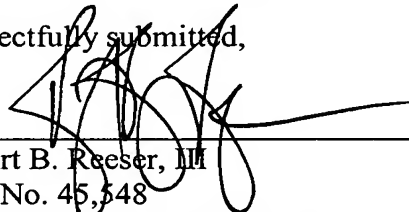
The correction is not due to any error by Applicants and no fee is due.

The Assignment for this patent is recorded on Reel 014939/Frame 0014.

Date: _____

Aug 3, 2007

Respectfully submitted,



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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 6,963,203 B2
APPLICATION NO. : 10/764,243
ISSUE DATE : November 8, 2005
INVENTOR(S) : Stanisic et al.

PAGE 1 OF 1

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 15, column 12, line 24, delete "programed" and insert therefor -- programmed --.

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SEP 6 2007